Form PTO-14 (REV. 8-83)	US Dept. of Commerc PATENT & TRADEMARK OFFIC		ATTY DO 100629.0	OCKET NO. 3	RULE 53(b) DIVISION OF APPLICATION NO. 10/267,834				
INFORMATION DISCLOSURE STATEMENT						•			
(Use several sheets if necessary)			APPLICANT(S) Takuroh OZAWA et al.						
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J.L.	5,714,838	2-19	998	HAIGHT et al.					
	6,072,450	6-2000		YAMADA et al.					
	5,587,329	12-96		HSEUH et al.					
	5,828,429	10-	98	TAKEMURA					
	6,023,307	02-	00	TAKEMURA					
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	6,011,529	01-	00	IKEDA					
	5,995,073	11-9	99	ISAMI et al.					
	6,243,069	06-0	01	OGAWA et al.					
J.X.	5,869,929	02-	99	EIDA et al.	lu .]			
	FOREI	GN P	ATENT DO	CUMENTS					
	DOCUMENT NUMBER	1	DATE	COUNT	RY	CLASS	SUB CLASS		
J.L.	EP-A1-0 653 741	EP-A1-0 653 741 05-9		Europe					
	JP-A-8-129358	05-9	96 Japan						
	JP 8-227276	09-9	96	Japan					
	JP 08 241057 A	09-9	96	Japan					
	EP 0112 700 A	07-1	34	Japan					
	WO 96 06456 A	02-9	96	WIPO					
	JP 08 129360 A	05-9	96	Japan					
	EP 0717 439 A	06-9	96	Europe					
J.7.	JP 08-241057 (translation only)	9-90	5	Japan					
	OTHER DOCUMENTS (In	cludin	g Author, T	litle, Date, Pertinent Page	s, etc.)	<u>:</u>			
J.L. J.L.	Alan G. Lewis, et al., Physical Mcchanisms For Short Channel Effects In Polysilicon Thin Film Transistors, IEEE, 1989, 13.4.1-13.4.4						E, 1989, pgs.		
J. L.	Alan G. Lewis et al., Polysilicon TFT Circuit Design and Performance, IEEE Journal of Solid-State Circuits, December 1992, Vol. 27, No. 12, pgs. 1833-1842.								
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Sheet 1 of 1

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Form PTO-1449 (REV. 8-83)	US Dept. of Commerce PATENT & TRADEMARK OFFICE			ATTY DOCKET NO. 100629.03				APPLICATION NO. 10/625,617			
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EXAMINER INITIAL		DOCUMENT NUMBER		DATE	NAME			CLASS	SUB CLASS		
J.L.	1	5,786,796	07/1998		TAKAYAMA et al.			345	76		
J.L.	2	5,986,632	11/1999		TAKAYAMA et al.			345	99		
J.L.	3	5,463,279	10/1995		KHORMAEI			315	169.3		
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		DOCUMENT NUMBER		DATE	COUNTRY			CLASS	CLASS		
J.L.	4	EP 0 653 741 Al	05/1995		EUROPE						
J.L.	5	JP 08-241057 (w/ English abstract)	09	/1996	JAPAN						
J.1.	6	WO 96/06456	02	/1996	WIPO				-		
T. L.	7	EP 0 112 700	07	7/1984	EUROPE			ļ			
J. L.	8	JP 08-129360 (w/ English abstract)	0.5	5/1996	JAPAN			ļ	ļ		
J.L.	9	EP 0 717 439 A2		5/1996	EUROPE						
					Title, Date, Pertinent Pages,						
J.L.	10	Lewis A. G. et al.: "Polysilicon TFT Circuit Design and Performance", IEEE Journal of Solid-State Circuits, IEEE Inc. New York, US, Vol. 27, No. 12, 1 December 1992							EE Inc. New		
									 		
J.L.	11	Lewis A. G. et al.: "Physical Mechanisms for Short Channel Effects in Polysilicon Thin Film Transistors", Proceedings of the International Electron Devices Meeting. Washington, Dec. 3-6, 1989, New York, IEEE, US									
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J.L.		04/1997	JAPAN			
J.L.	JP-A-10-12377 (w/ Eng. Abstract) 0	01/1998	JAPAN			<u> </u>
J.L.	EP 0 880 303 A1 1	11/1998	EUROPE			T =
J.L.	EP 0 849 721 A2 0	06/1998	EUROPE			
J.L.	·	12/1999	EUROPE			
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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)				ATTY DOCKET NO. 100629.03			APPLICATION NO. 10/625,617				
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J.L. J.L.	1	JP 04-161984 (Eng. Abstr Only)	06/0	5/1992	JAPAN	'AN		A			
J.L.	2	JP 08-129359(Eng.Trans&Abstr)	05/2	1/1996	JAPAN			Α			
J.L.	3	JP 09-016123(Eng.Trans&Abstr)	01/1	7/1997	JAPAN			A			
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Form PTO-1449 (REV. 8-83) PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)			CE	ATTY DOCKET NO. 100629.03				APPLICATION NO. 10/625,617		
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	<u> </u>	DOCUMENT NUMBER	DATE			COUNTRY		CLASS	SUB CLASS	
J.L.	1	JP 05-313195 (w/English-language translation)	11/	26/1993	JAPAN					
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Date: February 9, 2005

Examiner: